

MeiG-MA922

5G R16 + C-V2X

CPU ~20K DMIPS



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QDR3		
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C-V2X	V2X:4.75V~5.25V	: 5.0V
	VBAT: 3.3V-4.2V	: 3.8V
	VBAT: 3.3V-4.2V	: 3.8V

	1	
	1	
MIMO	2	
GNSS	1	
C-V2X	2	
DSDA	2	

SIM	2	
SDIO	2	DSRC eMMC v5.1
PCIe	2	BT/WIFI
USB	1	USB 3.1 Gen2 USB 2.0
UART	4	2
Debug UART	1	115200
SPI	2	
I2C	2	
I2S	2	
PCM	1	
RGMII/SGMII/USXGMII	1	USXGMII 2.5Gbps
ADC	3	
GPIO	15	

CPU	10K DMIPS
	15K DMIPS
	20K DMIPS
Flash	8Gb+8Gb MCP(nand+lpddr4x)
	4Gb+4Gb MCP(nand+lpddr4x)
	16Gb+16Gb MCP(nand+lpddr4x)
GNSS	L1+L5
	L1